

METHOD AND APPARATUS FOR A RECONFIGURABLE DIGITAL PROCESSOR FOR FILM CONVERSION

Abstract of the Disclosure

5 A reconfigurable digital processor to improve efficiency and flexibility in film
conversion applications is described. Analog signals corresponding to film images are
digitized and provided to the reconfigurable digital processor. The reconfigurable
digital processor uses programmable circuit elements, such as field programmable gate
10 arrays and digital signal processors, to perform film conversion functions. The number
of programmable circuit elements can be scaled in proportion to cost or desired
throughput rates, thereby offering a variety of cost-efficient film conversion devices for
varying needs. The reconfigurable digital processor provides one or more outputs
which are formatted to produce one or more digital motion picture files in parallel. The
15 reconfigurable digital processor can be altered using software codes at any time to
perform different film conversion functions and adapt to changes in format of digital
motion picture files.

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